

# Golajapu Jyothsna

D/o Sri G.V.Ramana  
Sarpavaram,  
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Andhra Pradesh - 533005.

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## EDUCATION:

Course	School/college	Board/University	Year of passing	% of marks
M.Tech (VLSI&ES)	JNTUK-UCEK, KAKINADA	JNTUK	2019	83.93
B. Tech (ECE)	JNTUK - UCEV, VIZIANAGARAM	JNTUK	2017	77.44
XII (MPC)	NARAYANA JUNIOR COLLEGE, KAKINADA	BIE	2013	95.9
X	OXFORD TALENT SCHOOL, KAKINADA	SSC	2011	91.33

- Qualified in the UGC-NET held on 08-07-2018 in the Subject of Electronic Science.
- Qualified in GATE-2023
- NPTEL Course- System Design through Verilog
- FDP- VLSI Exposure and its Challenges in Engineering

## DECLARATION:

I hereby assure you that the above-given information is entirely correct to the best of my knowledge.

Yours obediently,  
G.JYOTHSNA