



ADITYA ENGINEERING COLLEGE

An Autonomous Institution

Approved by AICTE • Permanently Affiliated to JNTUK • Accredited by NAAC with 'A' Grade
Recognised by UGC under sections 2(f) and 12(B) of UGC Act, 1956

Aditya Nagar, ADB Road, Surampalem - 533437, Near Kakinada, E.G.Dt., Ph:99498 76662

Department of Electronics and Communication Engineering

Date: 24-11-2019

Minutes of the V meeting of BOS scheduled on 23-11-2019.

The V meeting of Board of studies of Electronics and Communication Engineering Department was held on 23-11-2019 at 09.30 AM through online in Microsoft Teams. The Members discussed the agenda items and made the following resolutions.

Agenda 5.1: Welcome address by Chairperson- BOS.

Prof. G. Sridevi, BOS Chairperson invited the distinguished members of BOS to the VI BOS Meeting.

Agenda 5.2: Ratification of minutes of the previous Board of Studies meeting

The BOS members have ratified the points discussed in the previous Board of Studies meeting held on 20/07/2019.

Agenda 5.3: Discussion on proposed AR19 B.Tech (ECE) III semester syllabus and finalization of the same.

After long discussions with the BOS members on the proposed program AR19 III Semester course syllabus and the following suggestions are made:

- Suggested to make “Biology” course as “Engineering Biology” in particular.
- **Random variables and Stochastic Processes:** Suggested to discuss other department faculty about the topic “distribution and density functions” of “Random variables and Stochastic Processes” course.
- **Electronic Devices and Circuits:**
 - Suggested to include review on Semiconductor Physics in UNIT-1 of “Electronic Devices and Circuits” course.
 - Suggested to remove Electronic devices and Circuits by S Salivahanan from the list of Text books and include standard books.
 - Suggested to change the action verbs in course outcomes appropriately towards designing and assessment.
- **Signals and Systems:**
 - Suggested to include applications of Fourier analysis on signals in unit II.
 - Suggested to modify “Laplace and Z-transform properties of convolution” concept as “Convolution of signals through transforms” in unit III.

- Suggested to include reference books with software tool based orientation like MATLAB, PSPICE, LABVIEW etc.
- **Electronic Devices and Circuits Laboratory:** Suggested to include experiments on “V-I characteristics of Zener diode and Zener diode as a voltage regulator”, “half-wave and full-wave rectifiers” as single experiments respectively.
- **Digital electronics and Logic design Laboratory:**
 - Suggested to introduce code converters experiment using encoder and decoder circuits.
 - Suggested to modify the level of experiment title towards designing instead of analysis in specific.
- Suggested to include Open Source software tools as references for all Laboratory courses.
- Suggested to modify the order of Textbooks in “Digital Logic Design” course for Computer Science Engineering III semester.

Agenda 5.4: Analysis of Results.

The BOS Chairperson presented the odd and even semesters pass percentage for the A. Y. 2019-2020. The BOS members noted the same.

Agenda 5.5: Analysis of student feedback & action taken report

BOS Chairperson expressed that the student feedback & action taken report process was initiated at end of each semester. The BOS members noted the same.

Agenda 5.6: Any other items with the approval of Chairperson.

- Suggested to rethink about the Professional Elective-VI (MOOCS) to include before VIII semester as a benefit of student to avoid unwanted situations.
- Suggested to prepare a rubric to evaluate the examinations by appointing one teacher throughout the course.
- Suggested to modify the titles of open electives for other departments with appropriate syllabus to reduce the ambiguity.

Agenda 5.7: Scheduling of next Board of Studies meeting.

- The next BOS meeting is tentatively scheduled in the month of October 2020.

Agenda 5.7: Vote of Thanks:

Prof. G. Sridevi, BOS Chairperson presented the Vote of thanks.



BOS Chairperson

**Head of the Department
Department of E.C.E.
Aditya Engineering College (A9)**