

ADITYA ENGINEERING COLLEGE

An Autonomous Institution

Approved by AICTE • Permanently Affiliated to JNTUK • Accredited by NAAC with 'A' Grade

Recognised by UGC under sections 2(f) and 12(B) of UGC Act, 1956

Aditya Nagar, ADB Road, Surampalem - 533437, Near Kakinada, E.G.Dt., Ph:99498 76662

Department of Electronics and Communication Engineering

Action Taken Report (2021-22)

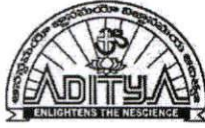
Name of the program: M. Tech (VLSI-Design)

Employer Feedback:

Suggestions given by the Employer and the action taken is presented in the following Table.

S. No	Recommendation based on Summarized Employer Feedback	Action Taken Report
1	Students will benefit from Industry institute interaction if facilitated.	Institute has signed MOUs with renowned industries to cater the students to aware of real time applications and recent trends in Industries.
2	ASIC & FPGA design methodologies, HVL: System Verilog, SVA, Verification Planning and Management, Code and Functional Coverage, Perl scripting language and VIP coding style are advanced courses. One can easily enter into the VLSI industry with the skill sets that are gained through these courses.	This suggestion will be taken forward to the concerned desk for necessary action to be taken.

Head of the Department
Head of the Department
Department of E.C.E.
Aditya Engineering College (A9)



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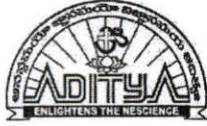
Name of the program: M. Tech (VLSI-Design)

Alumni Feedback:

Suggestions given by the Alumni and the action taken is presented in the following Table.

S. No	Recommendation based on Summarized Alumni Feedback	Action Taken Report
1	Encourage students to be a part of real time and live projects.	This suggestion will be taken forward to the concerned desk for necessary action to be taken.
2	Physical Design courses emphasizes on issues faced in industry level and how to resolve those issues. courses also focus on other aspects of VLSI back-end flow including Synthesis, IR drop analysis and Physical verification. Courses also will provide students with entire back-end flow, making sure that students fit in to various job requirements. Facilitate courses related to this.	This suggestion will be taken forward to the concerned desk for necessary action to be taken.

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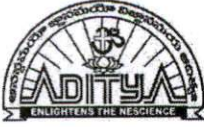
Name of the program: M. Tech (VLSI-Design)

Teacher Feedback:

Suggestions given by the Teacher and the action taken is presented in the following Table.

S. No.	Recommendation based on Summarized Parents Feedback	Action Taken Report
1	Students should be encouraged in taking active part in research and development.	This suggestion will be taken forward to the concerned desk for necessary action to be taken in such a way that the students can be involved in research activities.

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Name of the program: M. Tech (VLSI-Design)

Student Feedback:

Suggestions given by the student and the action taken is presented in the following Table.

S. No	Recommendation based on Summarized Student Feedback	Action Taken Report
1	Job oriented and skill-oriented courses related to the domain, if included in the curriculum will help students to a great extent.	This suggestion will be discussed in the BOS meeting and changes will be brought in the curriculum with proper approval.
2	The dynamic curriculum of Advance VLSI Design and Verification course fits perfectly with the career aim of fresh engineering graduates and helps them to 'future-proof' themselves and remain relevant for the rapidly evolving Semiconductor technology space. Include such courses in the curriculum.	This suggestion will be discussed in the BOS meeting and changes will be brought in the curriculum with proper approval.

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